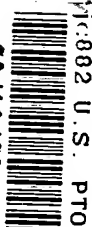


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PATENT APPLICATION
Attorney's Do. No. 2522-13

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ADRIENNE CHOCHOLAK
(SENDER'S PRINTED NAME)

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(SIGNATURE)

Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor Tae-Ho JANG:
For: SILICON-ON-INSULATOR (SOI) SUBSTRATE AND METHOD FOR
 MANUFACTURING THE SAME

Applicant requests FIG. 3C to be published with the application.

Enclosures:

- ☒ Specification (pages 1-8); claims (pages 9-11); abstract (page 12)
☒ TEN sheet(s) of FORMAL drawings
☒ Declaration or Combined Declaration and Power of Attorney
☒ Newly executed (original or copy)
☒ Assignment with cover sheet
Assignee Name and Address: Samsung Electronics Co., Ltd.
 Suwon-city, Kyungki-do, Korea
☒ Certified copy of Priority Document No. 2001-08111, filed February 19, 2001
☒ Return Postcard

CLAIMS AS FILED

For	Number Filed	Number Extra	Rate	Basic Fee \$740
Total Claims	18-20	0	x \$ 18 =	0
Independent Claims	2-3	0	x \$ 84 =	0
TOTAL FILING FEE				\$740



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Respectfully submitted,

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